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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Signature

Appl No. : 10/769,488  
Applicant : Shougo Imada, et al.  
Filed : January 30, 2004  
Title : MICROCOMPUTER LOGIC DEVELOPMENT

TC/A.U. : 2123  
Examiner : Dwain M. Craig

Docket No. : 51879/A400  
Customer No. : 23363

**PETITION TO CORRECT FILING DATE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
February 27, 2007

Commissioner:

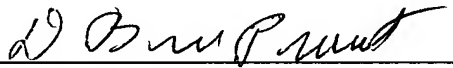
Attached herewith is a Filing Receipt for the subject application providing a filing date of January 29, 2004. However, the correct date deposited with Express mail of the U.S. Postal Service is January 30, 2004, and therefore the filing date should be January 30, 2004.

It appears that the filing date by the USPTO is based on the Express Mail "Origin (Postal use Only)" entries by the postal clerk, which shows a "Date In" of 1-29-04.

**Appln No. 10/769,488**

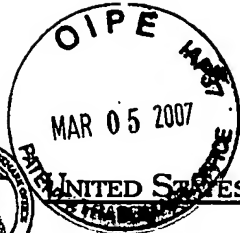
However, as noted by the enclosed Declaration of Alex Martinez, the documents transmitting the subject application to the U.S. Patent and Trademark Office were all prepared on the following day, January 30, 2004, and therefore could not have been deposited on January 29th. Accordingly, the filing date of the subject application should be corrected to January 30, 2004.

Respectfully submitted,  
CHRISTIE, PARKER & HALE, LLP

By   
D. Bruce Prout  
Reg. No. 20,958  
626/795-9900

DBP/mas  
Enclosure: Filing Receipt  
Declaration

AAM PAS597027.1-\*02/27/07 10:58 AM



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Christie, Parker &amp; Hale, LLP

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/769,488	01/29/2004	2825	928	51879/DBP/A400	25	24	4

CONFIRMATION NO. 8744

23363  
CHRISTIE, PARKER & HALE, LLP  
PO BOX 7068  
PASADENA, CA 91109-7068

## FILING RECEIPT



\*OC000000012959582\*

CASE # **A400:**  
**51879** ACTION \_\_\_\_\_  
REMINDER \_\_\_\_\_ DUE DATE \_\_\_\_\_  
DEADLINE \_\_\_\_\_

Date Mailed: 06/16/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Shougo Imada, Kobe-shi, JAPAN;  
Toshihiro Kashihara, Kobe-shi, JAPAN;  
Takashi Higuchi, Kobe-shi, JAPAN;

Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2003-024733 01/31/2003

If Required, Foreign Filing License Granted: 06/15/2004

Projected Publication Date: 09/23/2004

Non-Publication Request: No

Early Publication Request: No

## Title

Microcomputer logic development

Preliminary Class

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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